



## Silicon Labs Introduces First Single Port PoE Interface with Integrated dc-dc Controller

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AUSTIN, Texas--(BUSINESS WIRE)--Nov. 5, 2007--Silicon Laboratories Inc. (Nasdaq:SLAB), a leader in high-performance, analog-intensive, mixed-signal ICs, today announced the industry's first dual-function, single-port Power over Ethernet (PoE) controller with an integrated dc-dc controller for power sourcing equipment (PSE). By combining these two functions, the Si3460 dramatically simplifies development efforts, reduces system costs and eliminates specification compliance challenges for designers of a broad range of emerging PoE applications such as in home gateways, set-top boxes, cable modems, DSL modems and Voice over IP systems.

Unlike competing single-port solutions providing only a PSE interface, the Si3460 integrates a dc-dc digital power controller to cost effectively generate a negative 48V PSE supply, ensuring safety extra low voltage (SELV) compatibility with legacy telephony ports within the system. The Si3460 reduces the total PSE solution cost to less than US\$2.00 and preserves the use of low-cost, 12V power supply adapters and typical point-of-load (POL) dc-dc converters. The Si3460-EVB reference design, which includes all the recommended external components for a complete single-port PSE design, also provides IEEE 802.3af-compliant detection, classification and disconnect functionality.

The Si3460 controller provides extensive protection such as inrush current control, input under voltage lockout (UVLO), short circuit protection and current limiting to ensure robust, fail-safe operation during all operating conditions. The Si3460 also supports Fast (10/100 Mbps) and Gigabit Ethernet endpoints and midspans and is pin configurable to supply maximum power (15.4W, Class 0) PSEs, or limit power to IEEE class levels one through three.

"The Si3460 uniquely solves the two main challenges facing PSE systems designers: implementing a fully compliant IEEE 802.3 PSE interface and easily and safely generating the negative 48V PSE power supply from an existing low-voltage supply," said Dave Bresemann, vice president of Silicon Labs. "In combination with the Si3400/01 controller for PDs, Silicon Labs provides end-to-end solutions for PoE."

### Pricing and Availability

The Si3460 is sampling now with volume production in January 2008. The device is packaged in a RoHS compliant, Pb-free, 3 x 3mm QFN package. Pricing starts at \$1.05 each in quantities of 10K units. The comprehensive Si3460-EVB reference design kit is available now for \$95.00.

Silicon Laboratories Inc.

Silicon Laboratories is an industry leader in the innovation of high-performance, analog-intensive, mixed-signal ICs. Developed by a world-class engineering team with unsurpassed expertise in mixed-signal design, Silicon Labs' diverse portfolio of highly-integrated, easy-to-use products offers customers significant advantages in performance, size and power consumption. These patented solutions serve a broad set of markets and applications including consumer, communications, computing, industrial and automotive.

Headquartered in Austin, TX, Silicon Labs is a global enterprise with operations, sales and design activities worldwide. The company is committed to contributing to our customers' success by recruiting the highest quality talent to create industry-changing innovations. For more information about Silicon Labs, please visit [www.silabs.com](http://www.silabs.com).

### Cautionary Language

This press release may contain forward-looking statements based on Silicon Laboratories' current expectations. These forward-looking statements involve risks and uncertainties. A number of important factors could cause actual results to differ materially from those in the forward-looking statements. For a discussion of factors that could impact Silicon Laboratories' financial results and cause actual results to differ materially from those in the forward-looking statements, please refer to Silicon Laboratories' filings with the SEC. Silicon Laboratories disclaims any intention or obligation to update or revise any forward-looking statements, whether as a result of new information, future events or otherwise.

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