



Silicon Labs Introduces Industry's Highest Performance Synchronous Ethernet Clock IC

December 17, 2008 1:00 PM EST

Si5315 Dramatically Simplifies Frequency Conversion

AUSTIN, Texas--(BUSINESS WIRE)--Dec. 17, 2008--Silicon Laboratories Inc. (NASDAQ:SLAB), a leader in high-performance, analog-intensive, mixed-signal ICs, today introduced the expansion of its Any-Rate Precision Clock family with the Si5315, a jitter-attenuating clock multiplier IC that meets or exceeds the performance, integration, frequency and jitter requirements for the 1G and 10G Synchronous Ethernet (SyncE) market. The Si5315 is the industry's only SyncE clock multiplier IC to support 10G line encoding rates (161.13 MHz) in addition to SONET/SDH and Ethernet frequencies. The device requires no external phase-locked loop (PLL) components, dramatically simplifying line card design and frequency translation in carrier Ethernet switch routers (CESR), wireless backhaul, 3G/4G base stations, multi-service access platforms, passive optical networking, IP DSLAM and T1/E1 infrastructure.

As service providers upgrade legacy circuit switched networks to more cost-effective carrier Ethernet/IP networks, communications equipment suppliers face challenges when translating between legacy SONET/SDH and Ethernet clock frequencies. Existing SyncE clock ICs and voltage-controlled crystal oscillator-based module level solutions are limited by a combination of poor jitter performance, limited frequency flexibility and lack of integration, which ultimately make these traditional solutions difficult to use.

The Si5315 provides the lowest jitter of any SyncE clock IC in the industry at less than 0.6 ps rms phase jitter, meeting the jitter requirements specified by ITU-T G.8262 and providing significant margin to Gigabit Ethernet (GbE) and 10 GbE PHY jitter requirements. The Si5315 achieves this level of performance by leveraging Silicon Labs' patented DSPLL® technology to integrate all key components of a high-performance analog PLL on chip, thereby providing excellent immunity to system noise sources. For ease of use, the Si5315 generates more than 200 of the most popular frequency translations required in SyncE line card applications, simplifying design and reducing BOM cost and complexity.

"Current SyncE solutions are incomplete because they do not provide the breadth of frequencies and jitter performance to fully meet 10G requirements," said Dave Bresemann, vice president of Silicon Labs. "Silicon Labs' solution provides an unprecedented level of frequency flexibility, integration and performance, simplifying design and lowering costs for our customers."

In addition to this new family of SyncE clocks, Silicon Labs offers an expansive portfolio of frequency flexible, low jitter any-rate clock generators and buffers, jitter attenuating clock multipliers, XO/VCXOs, and silicon oscillators. These products use optimized DSPLL® and MultiSynth technology to shorten lead times and deliver outstanding jitter performance for datacom, telecom, wireless, broadcast video, test and measurement, and consumer markets.

Pricing and Availability

The Si5315 is packaged in a space-saving 36-lead, 6x6 mm QFN package with samples available in January and full production in Q1 2009. The Si5315A for 10G applications is priced at \$17.72 in 1,000 piece quantities. The Si5315B for 1G applications is priced at \$12.50 in 1,000 piece quantities.

Silicon Laboratories Inc.

Silicon Laboratories is an industry leader in the innovation of high-performance, analog-intensive, mixed-signal ICs. Developed by a world-class engineering team with unsurpassed expertise in mixed-signal design, Silicon Labs' diverse portfolio of highly integrated, easy-to-use products offers customers significant advantages in performance, size and power consumption. These patented solutions serve a broad set of markets and applications including consumer, communications, computing, industrial and automotive.

Headquartered in Austin, TX, Silicon Labs is a global enterprise with operations, sales and design activities worldwide. The company is committed to contributing to our customers' success by recruiting the highest quality talent to create industry-changing innovations. For more information about Silicon Labs, please visit www.silabs.com.

Cautionary Language

This press release may contain forward-looking statements based on Silicon Laboratories' current expectations. These forward-looking statements involve risks and uncertainties. A number of important factors could cause actual results to differ materially from those in the forward-looking statements. For a discussion of factors that could impact Silicon Laboratories' financial results and cause actual results to differ materially from those in the forward-looking statements, please refer to Silicon Laboratories' filings with the SEC. Silicon Laboratories disclaims any intention or obligation to update or revise any forward-looking statements, whether as a result of new information, future events or otherwise.

Note to editors: DSPLL, Silicon Laboratories, Silicon Labs, the "S" symbol, the Silicon Laboratories logo and the Silicon Labs logo are trademarks of Silicon Laboratories Inc. All other product names noted herein may be trademarks of their respective holders.

CONTACT: Silicon Laboratories Inc., Austin
Lindsey Starnes, +1 512-532-5349
lindsey.starnes@silabs.com

Source: Silicon Laboratories Inc.