Silicon Labs Introduces the Highest Performance Single Channel Telephony IC

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Si3217x ProSLIC® Family Reduces Board Area by 50 Percent for Broadband Networking Equipment

AUSTIN, Texas--(BUSINESS WIRE)--Jun. 22, 2009-- Silicon Laboratories Inc. (NASDAQ: SLAB), a leader in high-performance, analog-intensive, mixed-signal ICs, today announced the industry's highest performance, highest integration and lowest power family of single-channel foreign exchange station (FXS) solutions. The Si3217x ProSLIC® family is the first to integrate everything needed for an FXS interface into a single package, reducing board area by 50 percent without compromising the performance and diagnostic capabilities typical of traditional telephony service. It is the only single-package FXS solution to address all of the requirements for VoIP customer premise equipment (CPE) including density, power, global programmability, comprehensive testability, idle channel noise (ICN) and low cost per channel.

The Si3217x family raises the level of performance expected from traditional VoIP telephony ICs and eases system design by significantly reducing board area. These tiny 5 x 7 mm devices are the only solutions to integrate the SLIC and high-voltage line-feed together with a DSP, dc-dc controller and test load in a single package. Competing chipsets increase the bill of materials and power budget for VoIP systems. In addition, select Si3217x devices integrate the system-side portion of the world's most proven global DAA for the foreign exchange office (FXO) interface, resulting in the industry's first two-package FXS + FXO solution when used with the new Si3291x line-side DAA devices.

"The small footprint and low power consumption of Silicon Labs' new Si3271x ProSLIC will give customers longer battery standby time and better voice quality while reducing the cost of new Wireless Fixed Terminals," added Zhang Minghao, wireless access terminals system director for Zhong Xing Telecommunication Equipment Company Limited (ZTE Corporation).

The tiny Si3217x devices do not compromise performance. With ICN as low as 7 dBrnC, the Si3217x virtually eliminates audible noise on the line, and wideband audio capability provides high-definition sound quality surpassing traditional phone network performance, giving end users a clearer connection and a better experience.

The devices provide extensive support for remote system diagnostics: power alarms to warn of critical faults, a built-in test load for production testing or self test and comprehensive line diagnostic (MLT – Metallic Loop Testing, e.g. GR-909) capabilities. Unlike competing devices, the Si3217x has sense points inside and outside the protection circuit to ensure that diagnostic capability is not lost following a power cross or surge event. This unique combination of features further raises performance and cuts costs for carriers.

This best-in-class integration and performance is offered with the industry's lowest power consumption to reduce heat and lower power supply cost. The Si3217x prolongs backup battery life and enables "green" products with its ultra-low-power on-hook mode which consumes less than 70 mW when the telephone is not in use, and unlike competing products, this low level of power consumption can be achieved even if the ProSLIC is connected to a relatively long loop. An integrated, intelligent tracking dc-dc converter optimizes efficiency in all modes by generating only the voltage needed in each of the on-hook, off-hook and ringing states. Patented low-power ringing

permits this tiny Si3217x device to generate high-power ring signals on long lines with large ring loads.

“Silicon Labs continues to lead the mixed-signal telephony IC market, offering the industry’s best combination of high performance, high integration and low power,” said Carlos Garcia, general manager of access products for Silicon Laboratories. “This unique new family of ProSLIC devices enables the next generation of VoIP equipment to meet the ever-growing demands for high quality audio, low power, small size, and low total cost of ownership.”

Superior software and reference design support accelerates customers’ time to market and simplifies design. Silicon Labs’ ProSLIC API, a common software library for the entire ProSLIC portfolio, eliminates the need to develop system-specific software drivers for ProSLIC devices. To optimize cost versus efficiency, Si3217x evaluation boards (EVBs) support multiple dc-dc converter options. Additional EVBs and reference designs are available to demonstrate solutions for “re-injection” deployments of DSL using Silicon Labs’ Si84xx ISOpro digital isolators. To further alleviate design challenges, reference design hardware and software support is available from leading vendors of SoCs for VoIP CPE.

Pricing and Availability

The Si3217x ProSLIC family is available now in 5 x 7-mm, 42-pin QFN packages; the Si3291x line side DAA devices are available now in 16-pin SOIC packages. Si3217x pricing begins at $2.94 USD in quantities of 10K.

Silicon Laboratories Inc.

Silicon Laboratories is an industry leader in the innovation of high-performance, analog-intensive, mixed-signal ICs. Developed by a world-class engineering team with unsurpassed expertise in mixed-signal design, Silicon Labs’ diverse portfolio of highly integrated, easy-to-use products offers customers significant advantages in performance, size and power consumption. These patented solutions serve a broad set of markets and applications including consumer, communications, computing, industrial and automotive.

Headquartered in Austin, TX, Silicon Labs is a global enterprise with operations, sales and design activities worldwide. The company is committed to contributing to our customers’ success by recruiting the highest quality talent to create industry-changing innovations. For more information about Silicon Labs, please visit www.silabs.com.

Cautionary Language

This press release may contain forward-looking statements based on Silicon Laboratories’ current expectations. These forward-looking statements involve risks and uncertainties. A number of important factors could cause actual results to differ materially from those in the forward-looking statements. For a discussion of factors that could impact Silicon Laboratories' financial results and cause actual results to differ materially from those in the forward-looking statements, please refer to Silicon Laboratories’ filings with the SEC. Silicon Laboratories disclaims any intention or obligation to update or revise any forward-looking statements, whether as a result of new information, future events or otherwise.

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Silicon Laboratories Inc.
Shannon Pleasant, +1 512-464-9254
shannon.pleasant@silabs.com