



Silicon Labs Introduces Industry's Most Advanced SLIC Solution for Voice-over-IP Gateways

July 20, 2011 12:00 PM EDT

Si3226x Dual ProSLIC Family Reduces Cost, Board Size and Power Consumption for Wide Range of VoIP Customer Premises Equipment

AUSTIN, Texas--(BUSINESS WIRE)-- [Silicon Laboratories Inc.](#) (NASDAQ: SLAB), a leader in high-performance, analog-intensive, mixed-signal ICs, today announced the industry's most integrated, cost-effective and power-efficient subscriber line interface circuit (SLIC) solution for voice-over-IP (VoIP) gateways. The new Si3226x Dual ProSLIC® family delivers the smallest bill of materials and board area and the lowest power consumption of any dual SLIC available. The Si3226x family provides an ideal solution for low-channel-count VoIP customer premises equipment (CPE) such as fiber to the home (FTTH) gateways, DSL integrated access devices and cable embedded multimedia terminal adaptors (EMTAs), as well as high-channel-count products including multiple-dwelling-unit gateways and PBX systems.

According to Global Industry Analysts, FTTx deployment is expected to reach 183.9 million subscribers by 2015, with more than 80 percent of the roll-out occurring in Asia. The need for higher bandwidth to support IPTV and video-on-demand services, growing demand for bundled voice/data/video services and aggressive plans to upgrade copper networks to fiber-optic cable are driving the growth of the FTTx gateway market. Silicon Labs' Si3226x Dual ProSLIC family is designed to help reduce the cost and complexity of these gateways.

FTTx and other VoIP gateway manufacturers are challenged to fit all the functionality demanded by subscribers into small, inexpensive system designs. The Si3226x SLICs address this challenge by providing two complete channels in a tiny 60-pin 8 mm x 8 mm QFN package. The tiniest dual foreign exchange station (FXS) solution available, the Si3226x devices enable the lowest BOM cost and smallest footprint per channel for multiline gateways, even smaller than some quad- and octal-channel FXS solutions.

The Si3226x family is the industry's first FXS solution containing an integrated level shifter/driver that allows a direct connection to a dc-dc converter's power transistor regardless of input voltage. This innovation eliminates the need for the MOSFET pre-drive circuit required by competing SLIC designs, reducing the cost and footprint of a two-channel design by at least twelve external devices.

The Si3226x family sets a new benchmark in minimizing total FXS system power consumption, requiring less than 55 mW per channel when on-hook, making the Dual ProSLIC devices an ideal solution as telecommunication service providers and governments mandate more energy-efficient electronic products. The on-chip intelligent dc-dc converters optimize energy efficiency by generating only the voltages needed in on-hook, off-hook and ringing states. Ultra-low power consumption makes it easier to meet green energy requirements such as the European Commission's Code of Conduct on Power Consumption for Broadband Equipment; enables longer battery standby time in battery-backed designs; and reduces heat dissipation, eliminating the need for fans in multiline gateway designs, further reducing system cost.

The Si3226x family is designed to generate tracking batteries for lowest power consumption, as well as work with shared battery supplies (e.g., fixed-rail) for the lowest BOM cost. The integrated dc-dc controllers can be configured as either single-output tracking dc-dc converters for each SLIC channel or a single multi-rail dc-dc converter shared by two or more channels. This flexibility allows developers to use the same SLIC device for both low-power tracking designs and low-cost fixed-rail designs. Furthermore, Silicon Labs' innovative tracking shared supply (TSS) capability significantly reduces the power consumption of two-channel shared-battery designs compared to competitive fixed-rail designs.

"Silicon Labs continues to drive innovation in the mixed-signal telephony IC market by increasing on-chip integration, reducing power consumption, and shrinking BOM cost and component count with sophisticated dual SLIC designs," said Carlos Garcia, vice president and general manager of wireline products for Silicon Laboratories. "The Si3226x ProSLIC family is precisely what manufacturers need to develop the next generation of small-footprint, feature-rich and energy-efficient VoIP gateway designs."

Silicon Labs provides software and reference design support to accelerate time to market and simplify Si3226x-based designs. The ProSLIC API, a common software library for the entire ProSLIC portfolio, eliminates the need to develop system-specific software drivers for ProSLIC devices. To allow system designers to optimize the tradeoff between cost and power consumption, the Si3226x evaluation boards support multiple dc-dc converter options. To further ease design challenges, reference design hardware and software support is available from leading vendors of system-on-chip (SoC) solutions for VoIP gateway equipment.

Pricing and Availability

Silicon Labs offers two versions of the Si3226x Dual ProSLIC devices: those that connect to standard PCM/SPI digital interfaces or optionally to Silicon Labs' three-wire Integrated Serial Interface (ISI). The Si32260/1 standard PCM/SPI devices are available now in an 8 mm x 8 mm 60-pin QFN package. Additionally, the Si32266/7/8/9 devices are available in a 6 mm x 8 mm 50-pin QFN package and use Silicon Labs' ISI to reduce the interconnect to selected VoIP broadband gateway SoC solutions from several major vendors. Si3226x family pricing begins at \$5.31 (USD) in 10,000-unit quantities. For more information or to request samples, please visit www.silabs.com/pr/ProSLIC.

Silicon Laboratories Inc.

Silicon Laboratories is an industry leader in the innovation of high-performance, analog-intensive, mixed-signal ICs. Developed by a world-class engineering team with unsurpassed expertise in mixed-signal design, Silicon Labs' diverse portfolio of patented semiconductor solutions offers customers significant advantages in performance, size and power consumption. For more information about Silicon Labs, please visit www.silabs.com.

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Silicon Laboratories Inc.
Dale Weisman, +1-512-532-5871
dale.weisman@silabs.com

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