



Silicon Labs Introduces High-Performance Oscillators for Cloud Computing

May 29, 2013 12:00 PM EDT

Si535/536 Differential Crystal Oscillators Meet Ultra-Low Jitter Requirements of Data Center Switches, Routers and Storage Equipment

AUSTIN, Texas--(BUSINESS WIRE)-- [Silicon Labs](#) (NASDAQ: SLAB), a leader in high-performance, analog-intensive, mixed-signal ICs, today introduced a new family of crystal oscillators (XOs) that provide ultra-low jitter reference timing for 10G, 40G and 100G cloud computing and networking equipment. These new Si535 and Si536 XOs leverage Silicon Labs' proven [DSPLL® technology](#) to provide unparalleled performance, stability and flexibility for 10/40G data center core/access switches, storage area networking equipment, security routers, enterprise switches/routers, and Carrier Ethernet switches and routers.

To support the burgeoning demand for cloud computing-based services, data center equipment is migrating to higher speed serial data transmission, often 10G or faster. In parallel, there is a significant trend to maximize energy efficiency by consolidating switching, storage and computing resources into fewer components. These trends have given rise to processors, Ethernet switch ICs and FPGAs with integrated high-speed serializer-deserializer (SerDes) technology that requires low-jitter timing references. Silicon Labs' Si535/536 oscillators provide the ultra-low jitter and ± 20 ppm stability required by state-of-the-art cloud computing and networking infrastructure equipment.

The Si535/536 XOs offer exceptional jitter performance of < 200 femtoseconds (fs) RMS jitter (integrated from 10 kHz to 1 MHz) for common Ethernet and Fibre Channel reference frequencies. The Si535/536 oscillators support LVDS and LVPECL output formats at 2.5 V and 3.3 V and offer both ± 20 ppm and 31.5 ppm total stability, simplifying interfacing to a wide variety of processors, switches, PHYs and FPGAs. When combined with Silicon Labs' Si533xx differential clock buffers, the Si535/536 XOs provide low-jitter clock generation and distribution for SoCs requiring multiple high-performance reference clocks.

The Si535/536 oscillators use Silicon Labs' patented DSPLL technology to provide a low-jitter clock at high-speed differential frequencies. Unlike a traditional XO in which a different crystal is required for each output frequency, the Si535/536 XOs use a common fixed-frequency crystal to provide exceptional stability and reliability and leverage the DSPLL IC to generate any output frequency. In addition, DSPLL clock synthesis provides superior supply noise rejection, simplifying the task of generating low-jitter clocks in noisy environments typically found in data center and networking systems.

Oscillators based on DSPLL technology can be supplied at any output frequency without cutting and tuning a unique 3rd overtone (OT) crystal or surface acoustic wave (SAW) device, as required by competing low-jitter XOs. Silicon Labs' mixed-signal IC-based approach also enables simplified factory programming at time of shipment, eliminating long lead times associated with custom oscillators. Si535/536 XO samples ship in two weeks, the shortest lead time in the high-performance frequency control industry.

"Cloud computing switches, routers and storage equipment are moving to higher speed serial data links, increasing the necessity for high-performance timing," said Mike Petrowski, vice president and general manager of Silicon Labs' timing products. "We combine unparalleled performance, simple device customization and a highly efficient manufacturing flow to deliver both standard and custom any-frequency XOs with short, reliable lead times, streamlining product design cycles and eliminating supply chain headaches."

Pricing and Availability

Samples and production quantities of the Si535/536 low-jitter oscillators are available now. Product pricing for the Si535/536 XOs in 10,000-unit quantities ranges from \$8.24 to \$11.35 (USD), depending on frequency and stability options. The Si5XX-EVB evaluation board, priced at \$75 (USD MSRP), is available for device evaluation. For more information about the Si535/536 low-jitter oscillators or to order samples, please visit www.silabs.com/XO.

Silicon Labs

Silicon Labs is an industry leader in the innovation of high-performance, analog-intensive, mixed-signal ICs. Developed by a world-class engineering team with unsurpassed expertise in mixed-signal design, Silicon Labs' diverse portfolio of patented semiconductor solutions offers customers significant advantages in performance, size and power consumption. For more information about Silicon Labs, please visit www.silabs.com.

Cautionary Language

This press release may contain forward-looking statements based on Silicon Labs' current expectations. These forward-looking statements involve risks and uncertainties. A number of important factors could cause actual results to differ materially from those in the forward-looking statements. For a discussion of factors that could impact Silicon Labs' financial results and cause actual results to differ materially from those in the forward-looking statements, please refer to Silicon Labs' filings with the SEC. Silicon Labs disclaims any intention or obligation to update or revise any forward-looking statements, whether as a result of new information, future events or otherwise.

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