



Silicon Laboratories Names Chee-Keong Tan Managing Director of Asia Operations; Tan Brings Vast Experience in Business Strategy to Silicon Laboratories Inc.

June 12, 2006 10:00 AM EDT

SINGAPORE, Jun 12, 2006 (BUSINESS WIRE) -- Silicon Laboratories Inc. (Nasdaq:SLAB), a global leader in high-performance, analog-intensive, mixed-signal ICs, today announced the appointment of Chee-Keong Tan to the position of managing director of Silicon Laboratories' Asia operations.

Asia is a vital region for Silicon Laboratories, representing the majority of the company's sales. Mr. Tan brings more than 18 years of technical and operations management experience to the role and this expertise will allow the company to continue to effectively support operations throughout the region.

Prior to joining Silicon Laboratories, Mr. Tan held various leadership positions at United Test and Assembly Center Ltd. (UTAC), including serving as the group senior vice president of worldwide strategic test, engineering/development and supply management. In these capacities, he established several significant growth initiatives and oversaw the expansion of the mixed-signal operational group at UTAC.

Before joining UTAC, Mr. Tan was vice president of strategic test development at ST Assembly & Test Services Ltd., where he developed and then expanded the test business strategy.

"Mr. Tan is an experienced industry veteran who will continue to establish our operations in Asia," said Jon Ivester, vice president of worldwide operations for Silicon Laboratories. "Mr. Tan's leadership will also ensure our customers in Asia can continue to expect the highest quality, most reliable mixed-signal products."

Mr. Tan earned a bachelor's degree in electronic engineering from the University of London with Honors.

Silicon Laboratories Inc.

Silicon Laboratories Inc. is a leading designer of high-performance, analog-intensive, mixed-signal integrated circuits (ICs) for a broad range of applications. Silicon Laboratories' diverse portfolio of highly integrated, patented solutions is developed by a world-class engineering team with decades of cumulative expertise in cutting-edge mixed-signal design. The company has design, engineering, marketing, sales and applications offices throughout North America, Europe and Asia. For more information about Silicon Laboratories please visit www.silabs.com.

Cautionary Language

This press release may contain forward-looking statements based on Silicon Laboratories' current expectations. These forward-looking statements involve risks and uncertainties. A number of important factors could cause actual results to differ materially from those in the forward-looking statements. For a discussion of factors that could impact Silicon Laboratories' financial results and cause actual results to differ materially from those in the forward-looking statements, please refer to Silicon Laboratories' filings with the SEC. Silicon Laboratories disclaims any intention or obligation to update or revise any forward-looking statements, whether as a result of new information, future events or otherwise.

Note to editors: Silicon Laboratories, Aero and the Silicon Laboratories logo are trademarks of Silicon Laboratories Inc. All other product names noted herein may be trademarks of their respective holders.

SOURCE: Silicon Laboratories Inc.

Silicon Laboratories Inc., Austin
Kirstan Ryan, +1-512-532-5349
kirstan.ryan@silabs.com